FMC and General I/O

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NetFPGA-SUME

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[Diagram of NetFPGA-SUME with labeled components and connections]
The image contains a schematic diagram of a circuit. The diagram includes various components such as resistors, capacitors, and connectors labeled with numbers and symbols. The components are connected with lines indicating the electrical connections. The schematic is detailed and requires a technical understanding of electronic circuitry to interpret.

The diagram includes labels for different components, such as "GND," "PG_M2C," and "VCC1V8." These labels are typically used to denote ground, power, and supply voltages, respectively. The diagram also includes numerical labels, possibly indicating component values or connection points.

The diagram is part of a larger document, as indicated by the page numbers and section headings. The diagram is labeled "NetFPGA-SUME," suggesting it is related to a specific electronic project or application.
Mode Select:

- Loaded = JTAG
- No Load = Slave Select Map
Note: PCON_AUXFAULT_B, PCON_FAULT1 and PCON_ALERT_B must be internally pulled high.