

The Digilent Cmod S6 is a small, 48-pin DIP form factor board built around a Xilinx Spartan 6 LX4 FPGA. The board



also includes a programming ROM, clock source, USB programming and data transfer circuit, power supplies, and basic I/O devices. There are 46 FPGA I/O signals that are routed to 100-mil-spaced through-hole pins, making the Cmod S6 well suited for use with solderless breadboards. At just .7" by 2.6" inches, it can also be loaded in a standard socket and used in embedded systems.



#### • Xilinx Spartan<sup>™</sup>-6 FPGA

- Can be programmed with Xilinx's iMPACT tool or Digilent's Adept
- 46 FPGA GPIO signals brought to DIP pins
- 16Mbyte Spansion Quad SPI Flash for storing FPGA configurations and/or user data



The Cmod board combines a Xilinx CoolRunner II CPLD, a JTAG programming port, and power supply circuits in a



convenient 600-mil, 40-pin DIP package. Cmods are ideally suited for breadboard or other prototype circuit designs where the use of small surface mount packages is impractical.



### Xilinx CoolRunner II CPLD

- Once programmed, CPLD designs are non-volatile
- All available user I/O signals are brought out to DIP pins
- Adequate bypass capacitance
  on all CPLD voltage supply pins

## CoolRunner-II CPLD Starter Board

The CoolRunner-II Starter Board is a complete USB-powered circuit development platform for Xilinx's CoolRunner-II CPLD. The board includes highly-efficient power supplies, a programmable



oscillator, several I/O devices, and a USB2 port for board power and CPLD programming. The board also includes five expansion connectors that make 64 CPLD signals available to external circuits.



### Xilinx CoolRunner II CPLD

- USB 2.0 port for power, programming, & data transfer
- 1-wire DS28E01Q EEPROM
- 8MHz fixed-frequency oscillator
- 4 x 12-pin Pmod<sup>™</sup> connectors

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